



09/849707

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: Gregory J. Smith et al. Docket: 50019.57USU1/P04868  
Patent No.: US 6,775,112 B1  
Issued: August 10, 2004  
Title: APPARATUS AND METHOD FOR IMPROVING ESD AND TRANSIENT IMMUNITY IN SHUNT REGULATORS

**CERTIFICATE UNDER 37 CFR 1.8:**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on November 12, 2004.

By: 

Name: Brett A. Hertzberg

Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

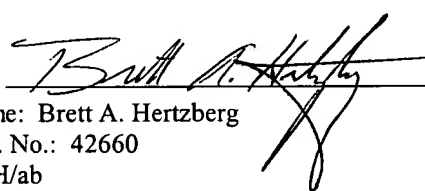
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Merchant & Gould P.C.  
P.O. Box 2903, Minneapolis, MN 55402-0903  
206.342.6200

By:   
Name: Brett A. Hertzberg  
Reg. No.: 42660  
BAH/ab

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By: 

Name: Brett A. Hertzberg  
Reg. No.: 42660  
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**23552**

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PATENT

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By: 

Name: Brett A. Hertzberg

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

Sir:

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. One copy of the text of the Certificate in the suggested form is enclosed.

As some of the errors listed are due to Applicants' mistake, the Commissioner is authorized to charge Deposit Account No 13-2527 in the amount of \$100.00 to cover the necessary fee in connection with this Certificate.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims, and re-examination is not required.

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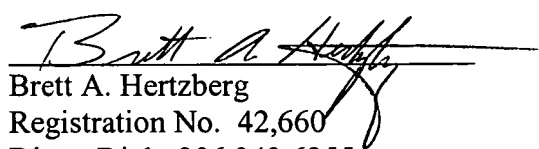
MERCHANT & GOULD  
P.O. 2903  
Minneapolis, MN 55402-0903

Respectfully submitted,

MERCHANT & GOULD

**23552**

PATENT TRADEMARK OFFICE

  
Brett A. Hertzberg  
Registration No. 42,660  
Direct Dial: 206.342.6255

NOV 23 2004

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

**PATENT NO. :** US 6,775,112 B1

**PAGE** 1 **of** 1

**DATED :** November 12, 2004

**INVENTOR(S) :** Gregory J. Smith et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page:

Beginning "(73) Assignee: National Semiconductor Corporation,  
Santa Clara, CA (US)"

should read --(73) Assignee: National Semiconductor Corporation,  
Santa Clara, CA (US); and  
Motorola, Inc.  
Schaumburg, IL (US)--

In the Abstract:

"and cause the active device to 'turn on'" should read --and cause the active device to "turn on."

Column 7, Line 20: "SO as to" should read --so as to--

Column 8, Line 6: "1.8. k $\Omega$ ." should read --1.8 k $\Omega$ --

Column 8, Line 23: "a GaAs, FET to JFET" should read --a GaAs, FET, JFET--

Column 10, Line 31: "inverter 160" should read --inverter I<sub>60</sub>--

Column 10, Line 38: "at node N<sub>6</sub>" should read --at node N<sub>61</sub>--

Column 11, Line 59: "ground terminal response to" --ground terminal in response to--

Column 11, Line 66: "the protection circuit fry" should read --the protection circuit further--

Column 12, Line 2: "the signal firm the power" should read --the signal from the power--

Column 12, Line 15: "slower than the fist" should read --slower than the fast"

Column 13, Line 6: "transient evens" should read --transient events--

Column 13, Line 56: "a slow ESD event an the power" should read --a slow ESD event on  
the power--

Column 14, Line 48: "to couple a fast signal" should read --to couple a fast transient signal--

**MAILING ADDRESS OF SENDER:**

Merchant & Gould P.C.  
Attn: Brett A. Hertzberg  
P.O. Box 2903  
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**Docket No.** 50019.57USU1/P04868

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